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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/497,107	02/03/2000	Jayne Brady	10457ROUS03U	7103
626	7590 09/12/2003			
	ETWORKS LIMITED		EXAMINER	
P. O. BOX 353 OTTAWA, ON CANADA	II, STATION C N KIY 4H7		HA, YVONN	IE QUY M
CANADA			ART UNIT	PAPER NUMBER
			2697	ſ
	•		DATE MAILED: 09/12/2003	6

Please find below and/or attached an Office communication concerning this application or proceeding.

*	Application No.	Applicant(s)				
	09/497,107	BRADY ET AL.				
Office Action Summary	Examiner	Art Unit				
	Yvonne Q. Ha	2697				
The MAILING DATE of this communication app Period for Reply	pears on the cover she	et with the correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	36(a). In no event, however, n y within the statutory minimum will apply and will expire SIX (6 o, cause the application to beco	nay a reply be timely filed of thirty (30) days will be considered timely.) MONTHS from the mailing date of this communication me ABANDONED (35 U.S.C. § 133).	on.			
1) Responsive to communication(s) filed on <u>09</u> .	July 2003 .					
<u> </u>	nis action is non-final.					
3) Since this application is in condition for allow		I matters, prosecution as to the merits	is			
closed in accordance with the practice under Disposition of Claims	Ex parte Quayle, 193	5 C.D. 11, 453 O.G. 213.				
4) \boxtimes Claim(s) <u>1-14</u> is/are pending in the application	າ.					
4a) Of the above claim(s) is/are withdra	wn from consideratior	1.				
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-14</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/o	or election requiremen	t.				
Application Papers	_					
9) The specification is objected to by the Examine		by the Eveniner				
10) ☐ The drawing(s) filed on is/are: a) ☐ acce Applicant may not request that any objection to the						
11) The proposed drawing correction filed on						
If approved, corrected drawings are required in re		disapproved by the Examiner.				
12) The oath or declaration is objected to by the Ex	. •					
Priority under 35 U.S.C. §§ 119 and 120						
13) Acknowledgment is made of a claim for foreign	n priority under 35 U.S	S.C. § 119(a)-(d) or (f).				
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority document	ts have been received					
2. Certified copies of the priority document						
Copies of the certified copies of the prio application from the International Bu See the attached detailed Office action for a list	rity documents have I Ireau (PCT Rule 17.2	peen received in this National Stage (a)).				
14) Acknowledgment is made of a claim for domest	•		tion).			
a) The translation of the foreign language pro	ovisional application h	as been received.	·			
Attachment(s)	priority arraor oo o					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) 🔲 Noti	rview Summary (PTO-413) Paper No(s) ce of Informal Patent Application (PTO-152) er:				

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DETAILED ACTION

Response to Amendment

1. Amended claims 1, 4, 10, and 12 have been entered.

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-2, 6, 8, 10-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Norman (US Patent 6,011,802) in view of Nakamura et al. (US Patent 6,385,213) in further view of Chao et al (US Patent 4,893,306).

Referring to claim 1, Norman discloses assembly of STM-N frame comprising the steps of receiving an administrative unit AU-n comprising a payload and an AU-n pointer (col.5; lines 8-36; Figure 2) and multiplexing TU-n into the STM-N frame (col.6; lines 52-62; Figure 2) and AU-n pointer provides the beginning of said payload with respect to the STM-N frame (col.5; lines 24-27). Norman does not expressly disclose the converting said AU-n to a tributary unit TU-n in hierarchical order. However, Nakamura discloses the conversion of AU-n payload to TU-n payload including the corresponding pointers (col.3; lines 23-29). Chao discloses multiplexing tributaries having different bit rates into a common bit stream resulted in the well-known hierarchical multiplexing plan with DS-1, DS-2, DS-3 signals (col.2, 28-34). At the time of the invention, it would have been obvious to a person of ordinary skill in the art to combine the teaching of Norman's assembly of STM-N frame with Nakamura's conversion of AU-n to

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TU-n and Chao multiplexing in hierarchical order. One of ordinary skill in the art would have been motivated to combine the conversion techniques because multiplexing in hierarchical order is well known in the art with different rates into a common bit stream and the potential benefits of facilitating conversions of payload data between SDH and SONET by the ability of demultiplexing STM-N frame having AU-n into TU-n.

Referring to claim 2, Norman discloses SDH interface that performs AU pointer and TU pointer translations are performed based on TU payload (col.15; lines 13-21) and a gateway converter that handles payload conversions between VTG and TUG. Norman does not expressly disclose the translation of AU payload into a TU payload in the gateway converter or SDH interface. However, Nakamura discloses the conversion of AU-n payload to TU-n payload including the corresponding pointers (col.3; lines 23-29). At the time of the invention, it would have been obvious to a person of ordinary skill in the art to combine the teaching of Norman's pointer translation with Nakamura conversion of AU-n payload to TU-n payload. One of ordinary skill in the art would have been motivated to combine the AU to TU payload conversion techniques as Norman's disclosed converter which supports TUG (i.e. TU payload) as the combination allows for AU payload (converted to TU payload) to be handled by the gateway converter.

Referring to claim 8, Norman discloses the hierarchically multiplexed STM-4 when n=4 and N=4 (col.7; lines 7-15; Figure 3; references VC-4, AU-4).

Referring to claim 11 and 12, Norman discloses assembly of STM-N frame comprising the steps of receiving an administrative unit AU-n comprising a payload and an AU-n pointer (col.5; lines 8-36; Figure 2) and SDH interface that performs AU pointer and TU pointer

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translations are performed based on TU payload (col.15; lines 13-21) and a gateway converter that handles payload conversions between VTG and TUG. Norman does not expressly disclose the translation of AU payload into a TU payload in the gateway converter or SDH interface in hierarchical multiplexing and the reduction of AU pointers of very high-speed synchronous transport signal STM-N. However, Nakamura discloses the conversion of AU-n payload to TU-n payload including the corresponding pointers (col.3; lines 23-29). Chao discloses multiplexing tributaries having different bit rates into a common bit stream resulted in the well-known hierarchical multiplexing plan with DS-1, DS-2, DS-3 signals (col.2, 28-34). At the time of the invention, it would have been obvious to a person of ordinary skill in the art to combine the teaching of Norman's pointer translation with Nakamura conversion of AU-n payload to TU-n payload and Chao multiplexing in hierarchical order. One of ordinary skill in the art would have been motivated to combine the AU to TU payload conversion techniques as Norman's converter which supports TUG (i.e. TU payload) as the combination allows for AU payload (converted to TU payload) to be handled by the gateway converter and multiplexing in hierarchical order is well known in the art with different rates into a common bit stream. The conversion capability further motivates one of ordinary skill in art to include lower hierarchy level (i.e. fine granularity) AU which is translated into TU that is subsequently hierarchy multiplexed into higher level AU which reduces AU pointers of very high speed synchronous transport signal STM-N. Higher level (i.e. coarse granularity) AU pointer is added to the higher level AU payload that forms a higher-level STM-N.

Referring to claims 13 and 14, Norman discloses the structure of STM-N (i.e. hierarchy capability inclusive of STM-4 STM-16, STM-64, and STM-256) by multiplexing multiples of

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AUG to achieve the desired STM level (col.6; lines 61-62, Figure 3; references AUG and STM-N).

4. Claim 4 is rejected under 35 U.S.C 103(a) as being unpatentable over Norman (US Patent 6,011,802) in view of Nakamura et al. (US Patent 6,385,213) in further view of Muller (US Patent 5,465,252).

Referring to claim 4, Norman and Nakamura disclose all aspects of the claimed invention but failed to teach the use of fixed stuff bits. However, Muller discloses the potential presence of fixed stuff bits in the TU-n payload (col.5; lines 9-10). At the time of the invention, it would have been obvious to a person of ordinary skill in the art to apply stuff bits for synchronizing to the local switching network clock.

5. Claim 10 is rejected under 35 U.S.C 103(a) as being unpatentable over Norman (US Patent 6,011,802) in view of Kivi-Mannila et al. (US Patent 5,539,750).

Referring to claim 10, Norman discloses the assembly of SDH signal (i.e. STM frame). Norman does not expressly disclose the use of concatenated payload and the corresponding pointers. However, Kivi-Mannila discloses the use of concatenated AU payload and corresponding AU pointer that are based on concatenated TU payloads and TU pointers (col.6; lines 36-48). At the time of the invention, it would have been obvious to a person of ordinary skill in the art to combine the teaching of Norman's assembly of STM-N frame with Kivi-Mannila use of concatenated payloads and corresponding payloads. One of ordinary skill in the art would have been motivated to combine the concatenation techniques to be capable of transmitting a digital broadband signal having a bit rate of an intermediate hierarchy level.

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Response to Arguments

6. Applicant's arguments with respect to claims 1, 2, 6, 8, and 10-14 have been considered but are most in view of the new ground(s) of rejection.

Allowable Subject Matter

7. Claims 3-5, 7, and 9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

- 8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
 - Heiles et al. (US Patent 5,579,310) discloses method for switching through digital signals
 - Mueller (US Patent 5,168,494) discloses method for transmitting a digital broadband signal in a tributary unit concatenation via a network of a synchronous digital multiplex hierarchy)
 - Sugawara (US Patent 5,555,248) discloses tandem connection maintenance
 - Scheffel et al. (US Patent 5,428,612) discloses synchronous transmission system
 - Oksanen et al. (US Patent 5,666,351) discloses method for disassembling and assembling frame structures containing pointers
 - Sihvola et al. (US Patent 5,724,342) discloses method for receiving a signal in a synchronous digital telecommunications system

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- Cochon et al. (US Patent 4,893,306) discloses method and apparatus for multiplexing circuit and packet traffic
- 9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yvonne Q. Ha whose telephone number is 703-305-8392. The examiner can normally be reached on Monday-Friday 7a.m.-4p.m. Eastern.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ricky Ngo can be reached on 703-305-4798. The fax phone numbers for the organization where this application or proceeding is assigned are 703-305-3988 for regular communications and 703-305-9051 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

YQH September 3, 2003

RICKY NGO